



S/N 09/820898

PATENTIN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Girish P. Ramanathan et al.

Examiner: Unknown

Serial No.: 09/820898

Group Art Unit: Unknown

Filed: March 30, 2001

Docket: 884.963US1

Title: SYSTEM FOR VARYING TIMING BETWEEN SOURCE AND DATA  
SIGNALS IN A SOURCE SYNCHRONOUS INTERFACE

Assignee: Intel Corporation

Customer No: 21186

REVOCATION AND POWER OF ATTORNEY  
CERTIFICATE UNDER 37 CFR § 3.73(b)

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Commissioner for Patents  
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Technology Center 2100

In accordance with 37 C.F.R. Section 1.36, M.P.E.P. Section 402.05 and 402.07, please  
revoke any existing Powers of Attorney, if any, and appoint the following attorneys and/or patent  
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Office in connection therewith:

Aldous, Alan  
Anderson, Robert D.  
Anglin, J. Michael  
Arora, Suneel  
Bacon, Shireen  
Barre, Michael  
Beale, Jay P.  
Beekman, Marvin L.  
Bianchi, Timothy E.  
Billion, Richard E.  
Black, David W.  
Brake, Edward  
Brennan, Thomas F.  
Burge, Ben  
Chadwick, Robin A.  
Chen, George  
Choi, Glen  
Clark, Barbara J.  
Clise, Timothy B.  
Cochran, David R.  
Cool, Kenneth J.  
Crawford, Ted A.  
Dahl, John M.  
DeLizio, Andrew  
Diehl, Robert  
Draeger, Jeffrey S.  
Drake, Eduardo E.  
Embretson, Janet E.

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Reg. No. 33,826  
Reg. No. 24,916  
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Reg. No. 40,594  
Reg. No. 39,665

Mates, Robert E.  
McCall, Molly  
McCrackin, Ann M.  
Mehrlie, Joseph P.  
Mennemeier, Larry  
Muller, Mark V.  
Nagy, Paul  
Nama, Prakash  
Nelson, A. James  
Nesheiwat, Michael J.  
Nicholls, Dennis A.  
Nielsen, Walter W.  
Padys, Danny J.  
Parker, Lanny  
Parker, J. K.  
Peacock, Gregg A.  
Pedersen-Giles, Alan  
Perdok, Monique M.  
Peret, Andrew R.  
Peterson, David C.  
Plimier, Michael D.  
Price, Lucinda G.  
Proksch, Michael A.  
Prout, William F.  
Reif, Kevin A.  
Sayles, Crystal D.  
Schumm, Sherry W.  
Schwegman, Micheal L.

Reg. No. 35,271  
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Faatz, Cindy	Reg. No. 39,973	Scott, Russell	Reg. No. 43,103
Forrest, Bradley A.	Reg. No. 30,837	Seddon, Ken	Reg. No. 43,105
Gagne, Christopher	Reg. No. 36,142	Seeley, Mark	Reg. No. 32,299
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Maki, Peter C.	Reg. No. 42,832	Wong, Sharon	Reg. No. 37,760
Malen, Peter L.	Reg. No. 44,894	Yates, Steven D.	Reg. No. 42,242
Martinez, Anthony	Reg. No. 44,223		

STATEMENT UNDER 37 CFR § 3.73(b)

Intel Corporation is the assignee of the entire right, title and interest in the patent application identified above by virtue of an assignment from the inventor filed July 10, 2001 and recorded on Reel 011969, Frames 0891 - 0893.

Pursuant to 37 C.F.R. §3.73(b) I hereby declare that I, David Simon, am empowered to sign this statement on behalf of Intel Corporation, the assignee.

Please direct all correspondence in this case to:

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.  
P.O. Box 2938  
Minneapolis, Minnesota 55402  
612-373-6900

Date January 18, 2004

By 

David Simon

Title: Chief Patent Counsel